



Docket No.: 050090-0564

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
ABE, SHUNICHI, et al. : Confirmation Number: 5595
Application No.: 10/798,345 : Group Art Unit: 2814
Filed: March 12, 2004 : Allowed: August 11, 2005
Examiner: Alonzo Chambliss
For: SEMICONDUCTOR APPARATUS UTILIZING A PREPARATORY STAGE
FOR A CHIP ASSEMBLY (As Amended)


LETTER SUBMITTING CORRECTED FORMAL DRAWING

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowability August 11, 2005, submitted herewith is the corrected formal drawing for Figure 1. The corrected formal drawing is indicated as "Replacement Sheet". Please substitute for Figures 1 as originally filed..

Respectfully,
McDERMOTT WILL & EMERY LLP


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**Please recognize our Customer No. 20277
as our correspondence address.**